



PIN	FUNCTION
1	DIO1
2	DIO2
3	DIO3
4	DIO4
5	EOI
6	DAV
7	NRFD
8	NDAC
9	IFC
10	SRQ
11	ATN
12	SHIELD
13	DIO5
14	DIO6
15	DIO7
16	DIO8
17	REN
18	GND
19	GND
20	GND
21	GND
22	GND
23	GND
24	LOGIC GND

NOTE: SEE SPEC. SHEET FOR ATTENUATION STEPS (A=LSB G=MSB)

NOTE: CHASSIS DEPTH = 8.72

JFW INDUSTRIES, INC. INDIANAPOLIS, IN												PROPRIETARY INFORMATION				
REV	ECN	DATE	BY	REV	ECN	DATE	BY	REV	ECN	DATE	BY	DRAWN BY	PAUL TACKETT	DATE	4-15-96	ALL SURFACES ARE TO BE FREE OF BURRS, PITS, & SCRATCHES
A	5074	9-12-96	PT	L	5996	5-3-99	TEZ	W	7643	11-18-03	BZ	APP'D BY		SCALE	1/2	TOLERANCES
B	5205	2-24-97	TEZ	M	6046	6-18-99	PT	X	11525	7-21-05	LT	MATERIAL	ALUMINUM ALLOY			FRACTIONAL DIM. ± 1/64
C	5321	7-11-97	CDJ	N	6158	12-22-99	PT	Y	11646	8-5-05	LT	FINISH	CHEMFILM TCP/FRONT: LIGHT GREY			2 PLACE DECIMAL ± .010
D	5374	8-25-97	CDJ	P	6183	2-9-00	TEZ	Z	13384	9-15-06	BZ	DESCRIPTION	50PA: RACK MOUNT PROG. ATTEN.			3 PLACE DECIMAL ± .005
E	5735	7-13-98	CDJ	Q	6437	10-6-00	PT									ANGULAR DIM. ± 1/2°
F	5745	7-27-98	CDJ	R	6520	12-06-00	CDJ									DWG NO.
G	5746	7-29-98	CDJ	S	6571	1-10-01	CDJ									092-2441
H	5910	1-28-99	PT	T	6711	4-27-01	CDJ									
J	5910	3-16-99	PT	U	7137	6-3-02	TEZ									
K	5970	3-26-99	PT	V	7159	6-20-02	TEZ									