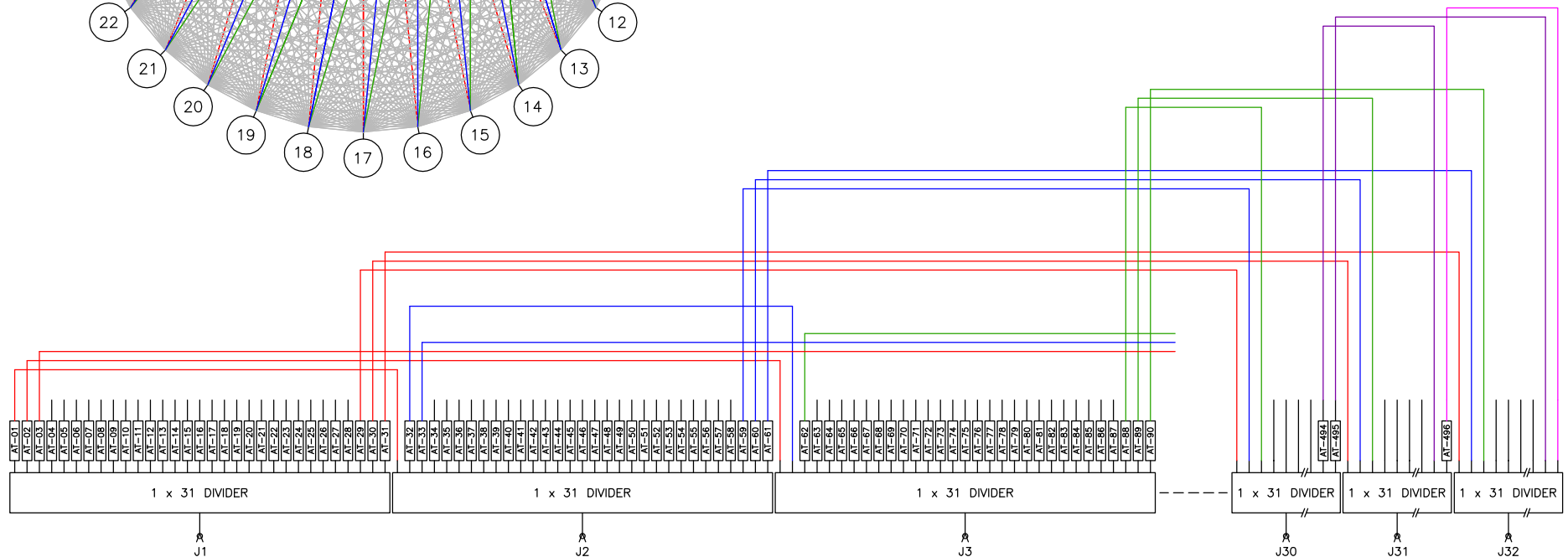



THIS IS A 32 PORT TRANSCEIVER TEST SYSTEM.  
 IT IS FULL FAN-OUT.  
 EVERY PORT CONNECTS TO EVERY OTHER PORT VIA 496 INTERNAL RF PATHS.  
 IT HAS A PROGRAMMABLE ATTENUATOR ON EVERY RF PATH (496 TOTAL ATTENUATORS)



REV	ECN	DATE	BY	APPR		<b>JFW INDUSTRIES, INC.</b> <a href="http://www.jfwindustries.com">www.jfwindustries.com</a>	PH: 317-887-1340
-	ISSUED	3-17-11	BZ	TR			
A	26712	6-13-16	DTS	TR			
					MATERIAL		PROPRIETARY INFORMATION
					FINISH		ALL SURFACES ARE TO BE FREE OF BURRS, PITS, & SCRATCHES
					MODEL	50PMA-XXX	TOLERANCES XX±.010 XXX±.005 YYY±.5'
					DESCRIPTION		DWG #
					32 PORT TRANSCEIVER TEST SYSTEM		092-6888