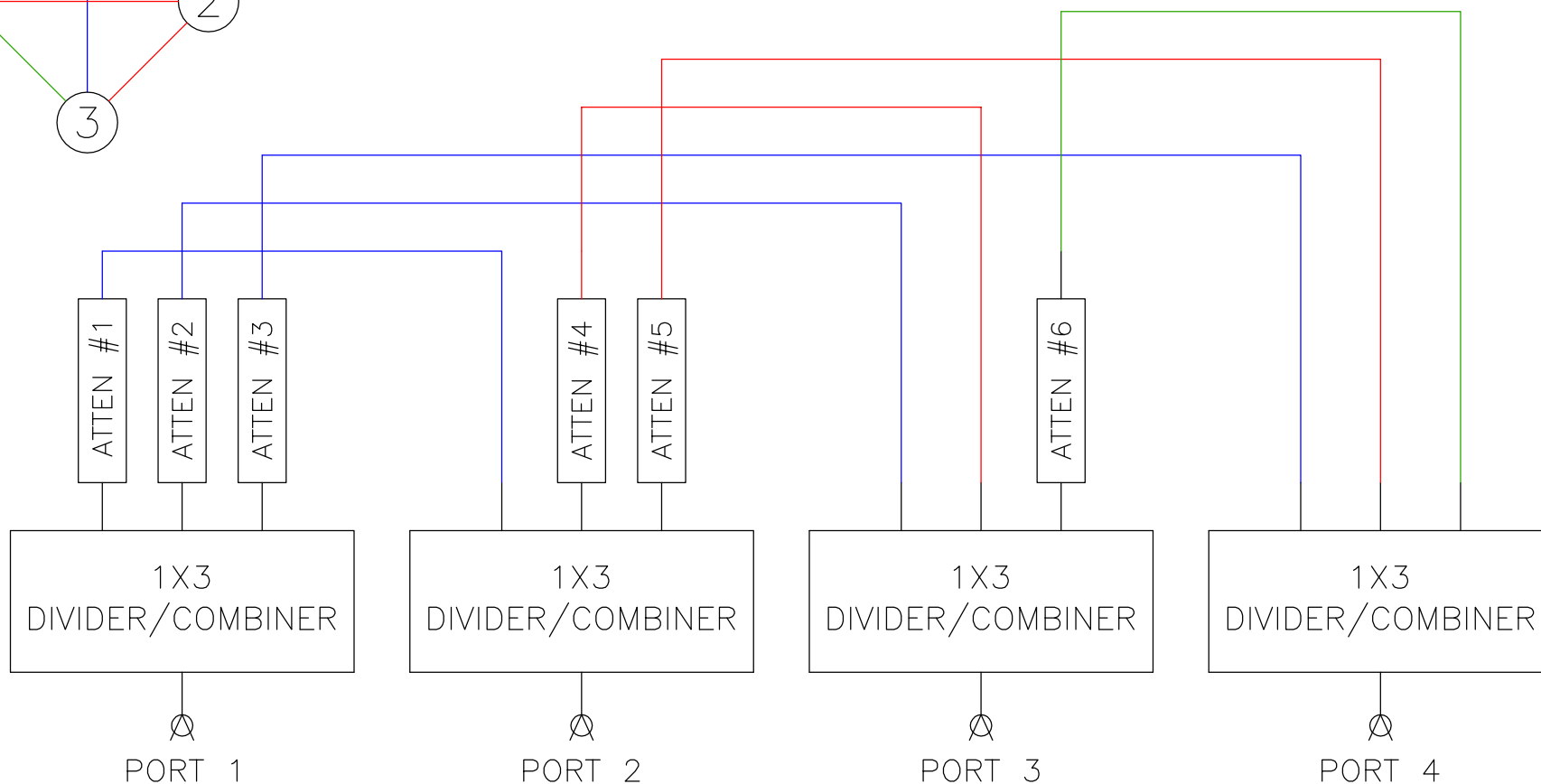



THIS IS A 4 PORT TRANSCEIVER TEST SYSTEM.  
 IT IS FULL FAN-OUT.  
 EVERY PORT CONNECTS TO EVERY OTHER PORT VIA 6 INTERNAL RF PATHS.  
 IT HAS AN ATTENUATOR ON EVERY RF PATH (6 TOTAL ATTENUATORS).



REV	ECN	DATE	BY	APPR	 <b>JFW INDUSTRIES, INC.</b> <a href="http://www.jfwindustries.com">www.jfwindustries.com</a> PH: 317-887-1340	
-	ISSUED	6-1-11	DTS	TRS		
A	26712	6-13-16	DTS	TRS		
					MATERIAL	PROPRIETARY INFORMATION
					FINISH	ALL SURFACES ARE TO BE FREE OF BURRS, PITS, & SCRATCHES
					MODEL	TOLERANCES
						.XX±.010 .XXX±.005 XX'±.5'
					DESCRIPTION	DWG #
					4 PORT TRANSCEIVER TEST SYSTEM BLOCK DIAGRAM	
					092-6984	