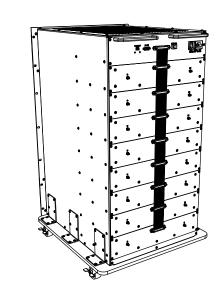


DETAIL A DE-9S

PIN	FUNCTION		
1	CHASSIS GROUND		
2	RECIEVED DATA		
3	TRANSMITTED DATA		
4	NO CONNECTION		
5	SIGNAL GROUND		
6	NO CONNECTION		
7	RTS		
8	CTS		
9	NO CONNECTION		



NOTE:
IF THE CASTORS AND BOTTOM PLATE ARE REMOVED
THE CHASSIS WILL MOUNT IN A 19 INCH RACK CABINET.

REV	ECN	DATE	BY	APPR		TEVAL INTE	LICTE	NEC INC
-	ISSUED	2-4-16	TEZ	TRS	(jfv	ا JFVV INL	ハクリト	RIES, INC.
Α	26718	6-14-16	TEZ	TEZ				
						www.jfwindustries.d	PH: 317-887-1340	
					ALUMINUM ALLOY  FINISH CHEMFILM TCP / FRONT: BLACK		PROPRIETARY INFORMATION	
							ALL SURFACES ARE TO BE FREE OF BURRS, PITS & SCRATCHES	
					MODEL	50PMA-XXX	SCALE	TOLERANCES .XX±.010 .XXX±.005 XX°±.5°
					DESCRIPTION			DWG#
					16 PORT 1	TRANSCEIVER TEST S	SYSTEM	092-7369-02