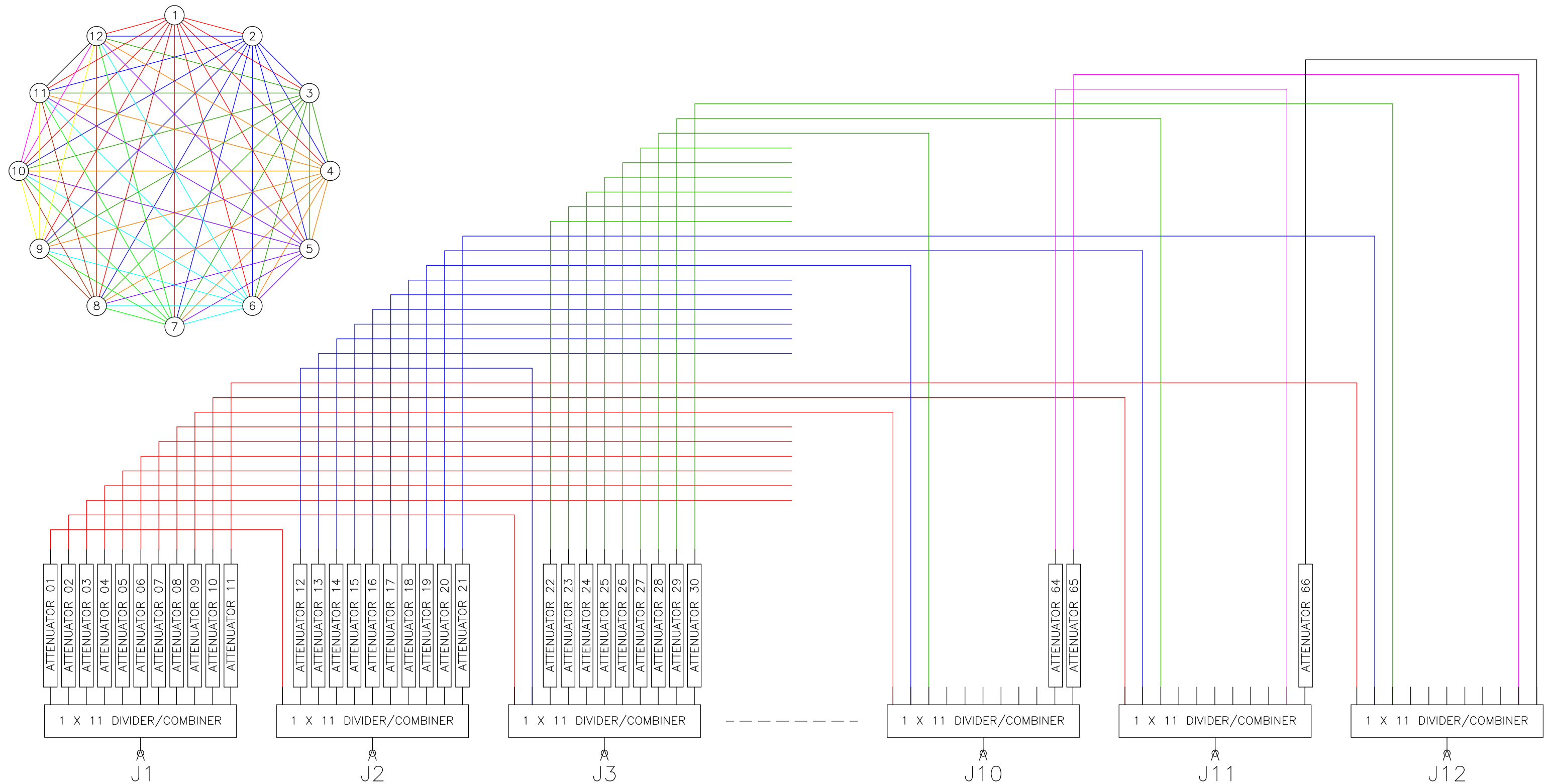




THIS IS A 12 PORT TRANSCEIVER TEST SYSTEM.
IT IS FULL FAN-OUT.
EVERY PORT CONNECTS TO EVERY OTHER PORT VIA 66 INTERNAL RF PATHS.
IT HAS A PROGRAMMABLE ATTENUATOR ON EVERY RF PATH (66 TOTAL ATTENUATORS).

REV	ECN	DATE	BY	APPR	 JFW INDUSTRIES, INC. www.jfwindustries.com PH: 317-887-1340	
-	ISSUED	1-23-13	DTS	TRS		
A	26712	6-13-16	DTS	TRS		
					MATERIAL	PROPRIETARY INFORMATION
					FINISH	ALL SURFACES ARE TO BE FREE OF BURRS, PITS, & SCRATCHES
					MODEL	TOLERANCES
					DESCRIPTION	.XX±.010 .XXX±.005 XX'±.5'
					12 PORT TRANSCEIVER TEST SYSTEM	DWG #
						092-7585