


PIN	FUNCTION
1	CHASSIS GROUND
2	RECEIVED DATA
3	TRANSMITTED DATA
4	NO CONNECTION
5	SIGNAL GROUND
6	NO CONNECTION
7	RTS
8	CTS
9	NO CONNECTION

NOTE: SEE JFW DRAWING# 092-7674 FOR BLOCK DIAGRAM

REV	ECN	DATE	BY	APPR	 <b>JFW INDUSTRIES, INC.</b>	
-	ISSUED	4-14-15	TEZ	TRS	www.jfwindustries.com PH: 317-887-1340	
					MATERIAL	ALUMINUM ALLOY
					FINISH	CHEMFILM TCP / FRONT: BLACK
					MODEL	50PMA-XXX
					DESCRIPTION	TRANSCEIVER TEST SYSTEM
					PROPRIETARY INFORMATION	
					ALL SURFACES ARE TO BE FREE OF BURRS, PITS, & SCRATCHES	
					TOLERANCES	
					.XX±.010 .XXX±.005 XX'±.5'	
					DWG #	
					092-7680-01	