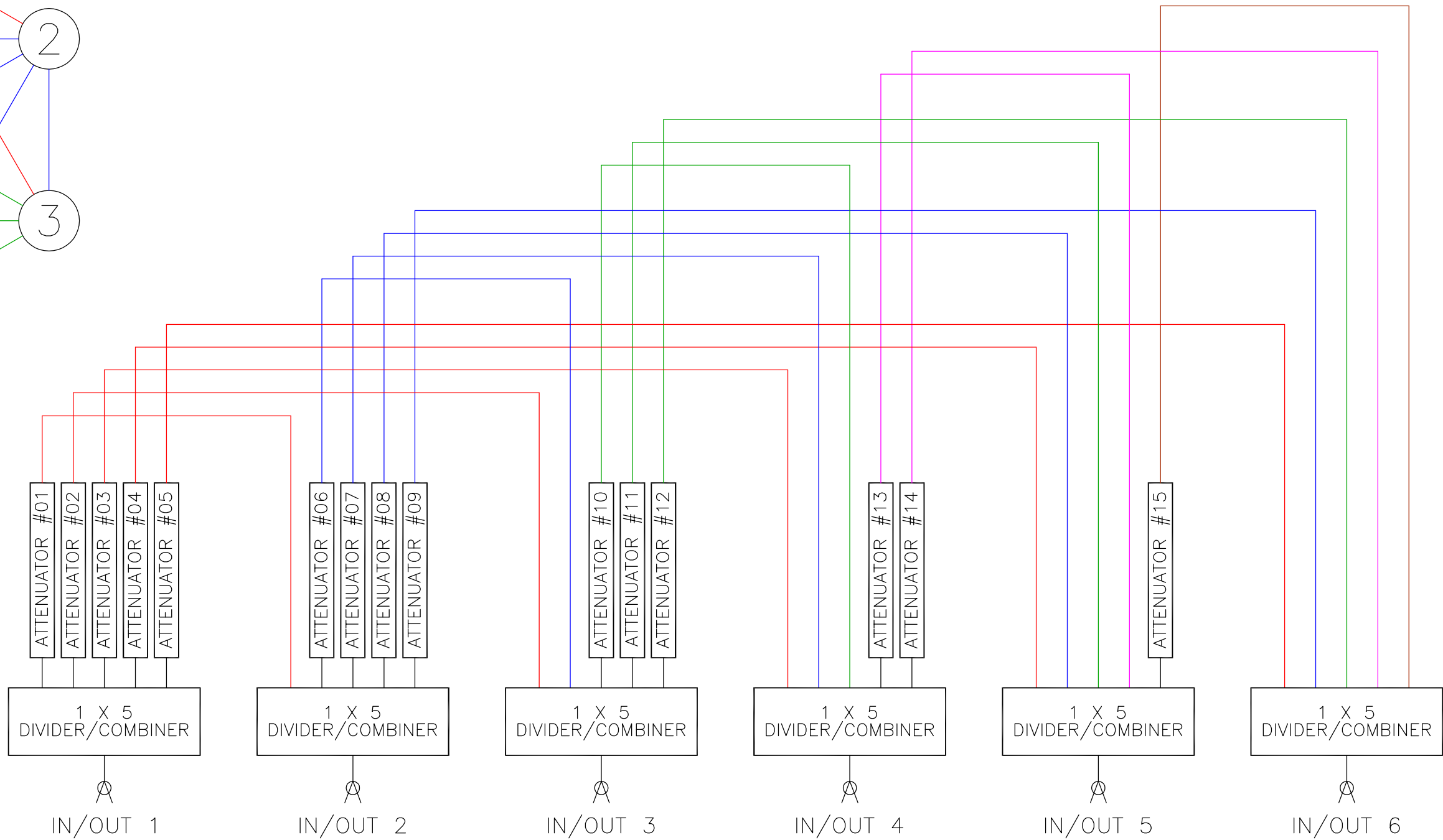
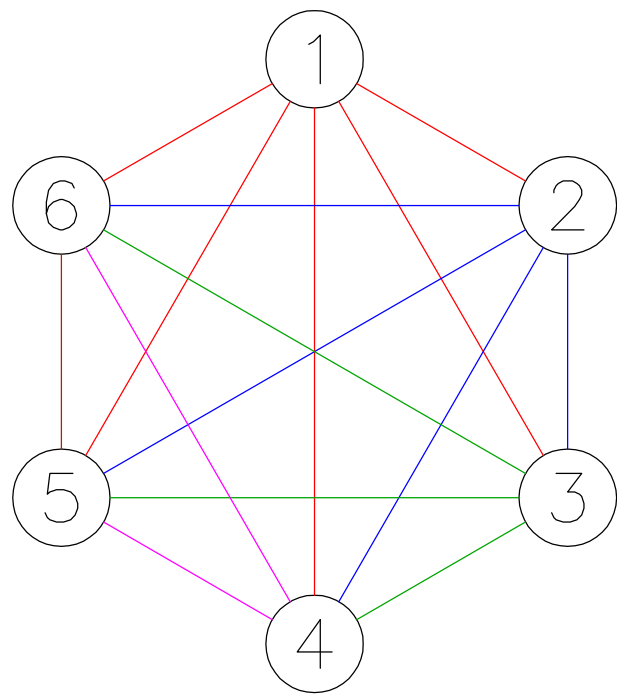




THIS IS A 6 PORT TRANSCEIVER TEST SYSTEM.
IT IS FULL FAN-OUT.
EVERY PORT CONNECTS TO EVERY OTHER PORT VIA 15 INTERNAL RF PATHS.
IT HAS AN ATTENUATOR ON EVERY RF PATH (15 TOTAL ATTENUATORS).

REV	ECN	DATE	BY	APPR	JFW INDUSTRIES, INC. www.jfwindustries.com PH: 317-887-1340	
-	ISSUED	5-1-13	TEZ	TEZ		
A	23476	11-25-13	JZ	TRS		
B	26712	6-13-16	DTS	TRS		
					MATERIAL	PROPRIETARY INFORMATION
					FINISH	ALL SURFACES ARE TO BE FREE OF BURRS, PITS, & SCRATCHES
					MODEL	TOLERANCES .XX±.010 .XXX±.005 XX'±.5'
					DESCRIPTION	DWG #
					6 PORT TRANSCEIVER TEST SYSTEM BLOCK DIAGRAM	
					092-7697	