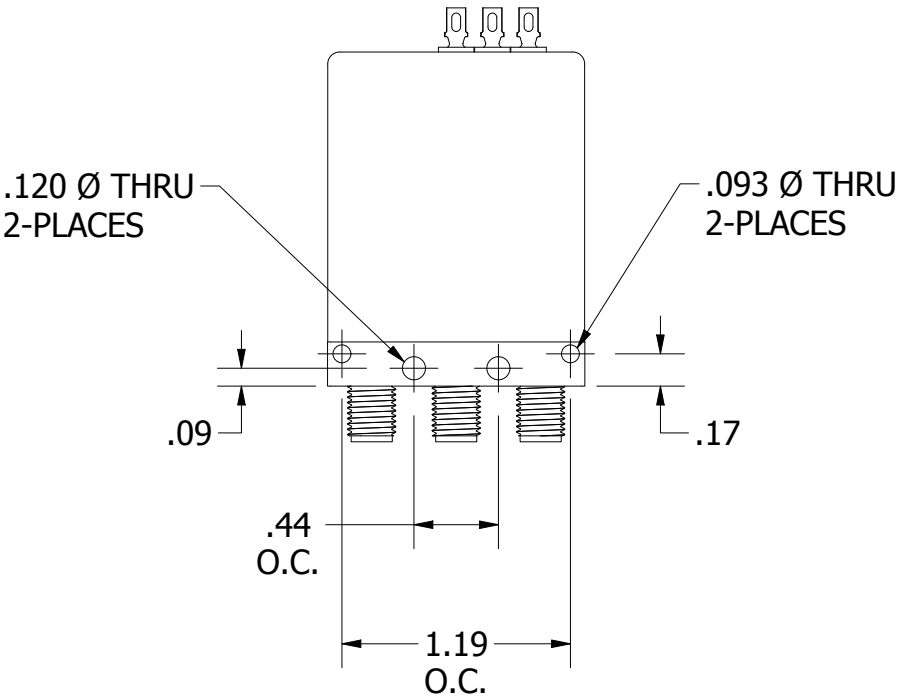
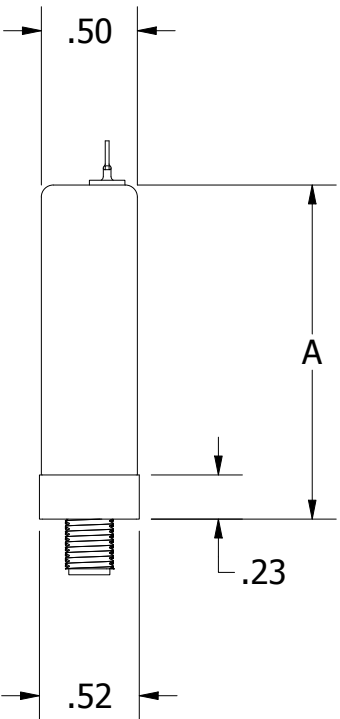
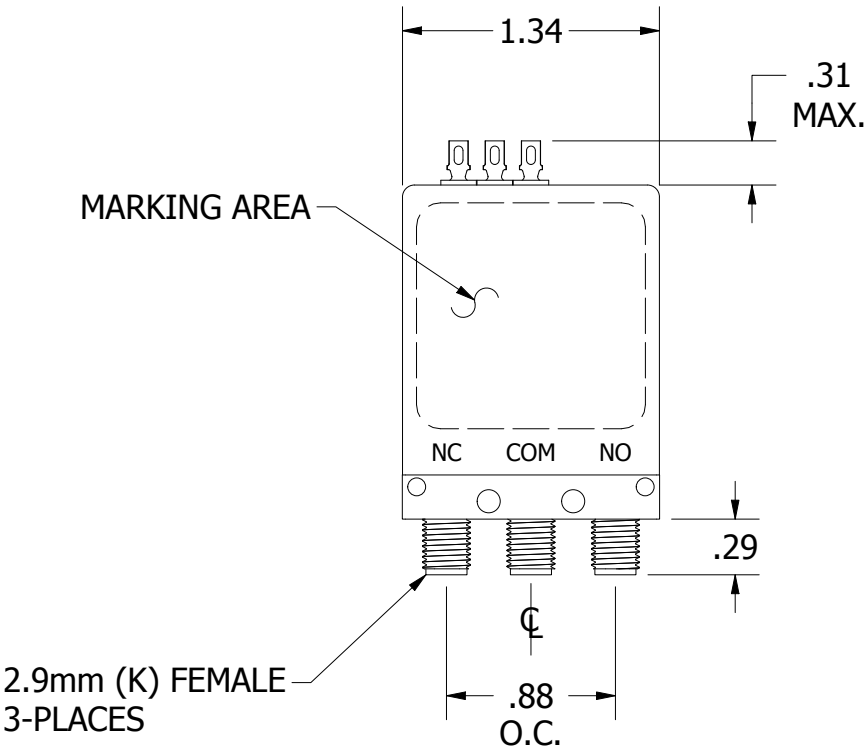
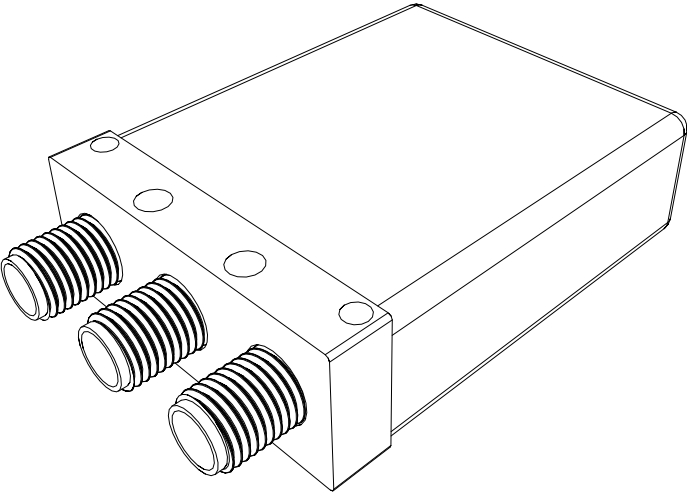




DC PIN	NON-TTL	TTL
1	+Vdc	+Vdc
2	GND	GND
3	NA	CONT.

CONTROL	DIM. A
NON-TTL	1.33
TTL	1.74

SUPERSEDING TOLERANCES
.XXX ±.01
.XX ±.03

REV	ECN	DATE	BY	APPR	 JFW INDUSTRIES, INC. www.jfwindustries.comPH: 317-887-1340	
-	ISSUED	4-4-16	JZ	TEZ		
A	27790	5-15-17	TEZ	TEZ		
B	29610	6-21-19	JZ	BAC		
					MATERIAL	PROPRIETARY INFORMATION
					FINISH	ALL SURFACES ARE TO BE FREE OF BURRS, PITS & SCRATCHES
					MODEL	TOLERANCES
					50S-XXXX	.XX±.010 .XXX±.005 XX°±.5°
					DESCRIPTION	DWG #
					1P2T FAILSAFE RF SWITCH	092-8603