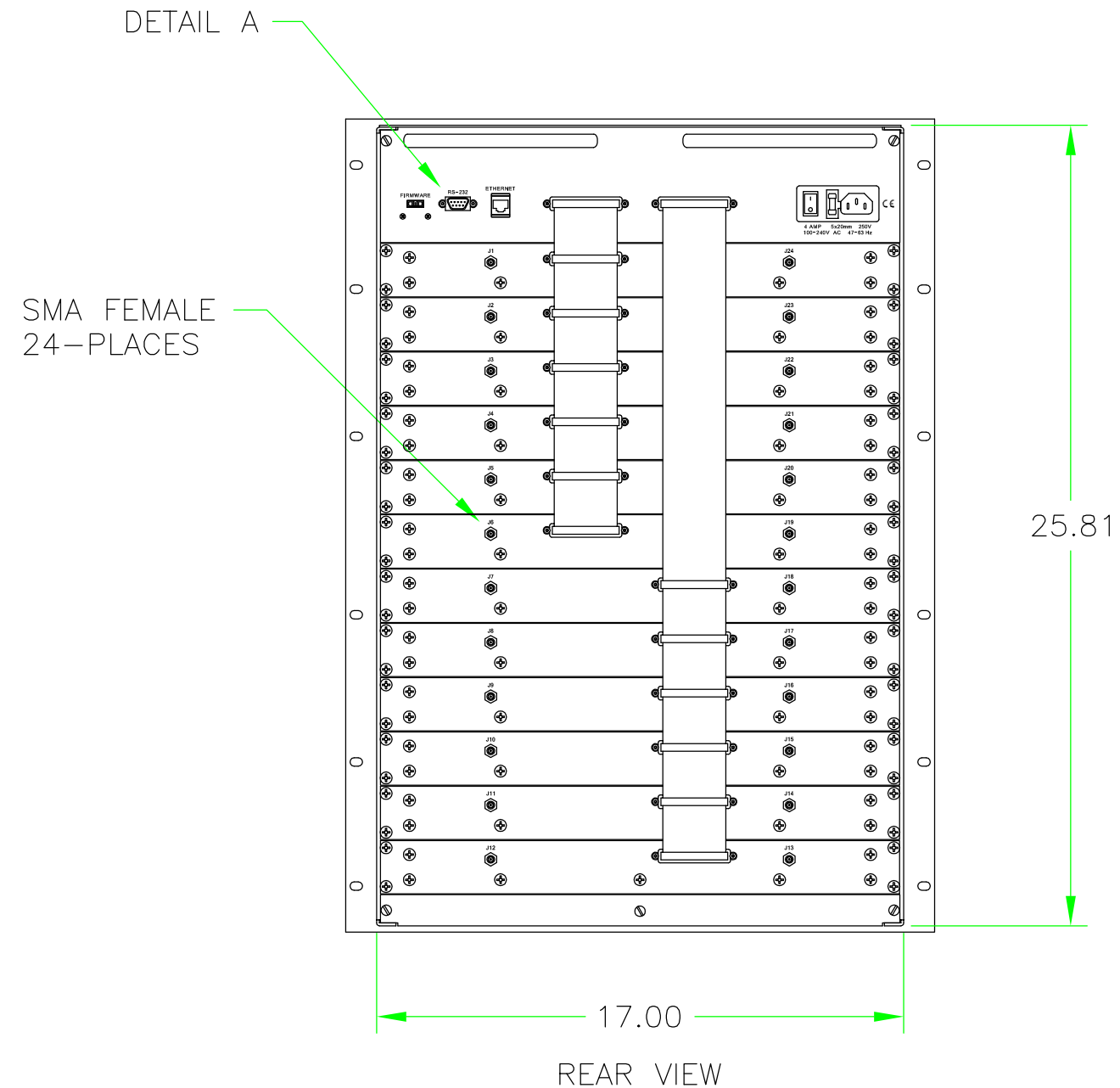
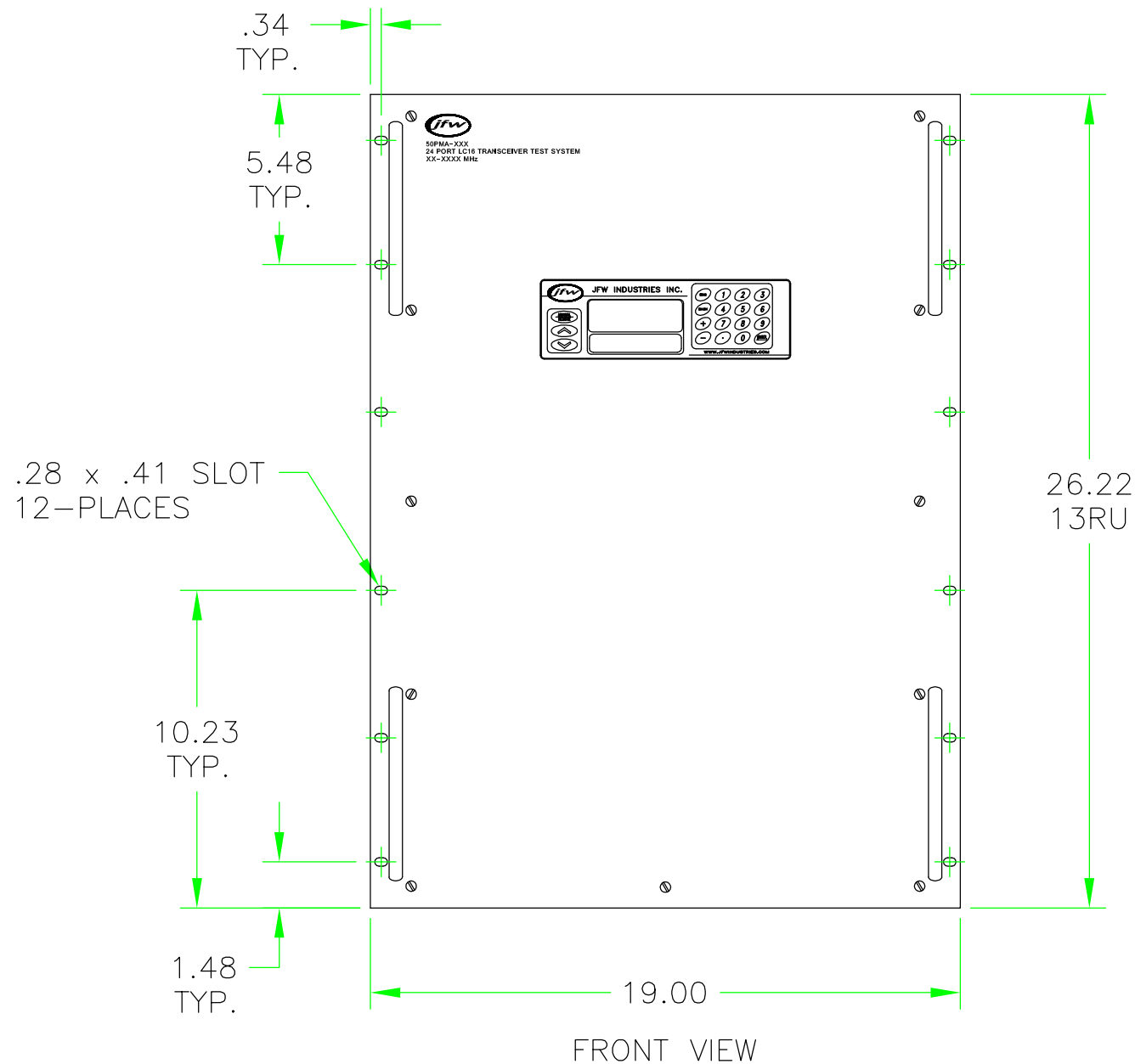
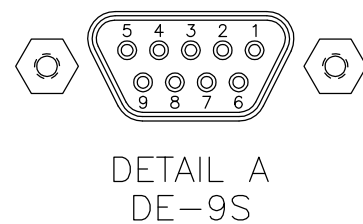




PIN	FUNCTION
1	CHASSIS GROUND
2	RECEIVED DATA
3	TRANSMITTED DATA
4	NO CONNECTION
5	SIGNAL GROUND
6	NO CONNECTION
7	RTS
8	CTS
9	NO CONNECTION



CHASSIS DEPTH = 21.47

NOTE: SEE JFW# 092-8617 FOR BLOCK DIAGRAM

REV	ECN	DATE	BY	APPR	 JFW INDUSTRIES, INC. www.jfwindustries.com PH: 317-887-1340	
-	ISSUED	4-26-16	TEZ	TRS		
					MATERIAL	ALUMINUM ALLOY
					FINISH	CHEMFILM TCP / FRONT: BLACK
					MODEL	50PMA-XXX
					DESCRIPTION	24 PORT LC16 TRANSCEIVER TEST SYSTEM
					SCALE	3/16
					TOLERANCES	.XX±.010 .XXX±.005 XX'±.5'
					DWG #	092-8616