Volatility Statement for Ethernet and RS-232 Test Systems

JFW Ethernet and RS-232 test systems consists of non-volatile Flash memory, volatile SRAM and a block of battery backed RAM. The firmware is stored in 512k of non-volatile Flash memory during manufacturing and is not accessible to the user. The operating code then runs in 512k of SRAM. Most variables that are stored when the program is running are stored in 512k of volatile SRAM. Any information that is stored during the operation of the unit in the volatile SRAM is cleared when the power is cycled.   The following items are not cleared on power cycle. Some variables are stored in the flash memory: Any fixed IP Address, Netmask, Gateway information, serial baud rate, Attenuator startup states if that option is selected by the user and other configuration information.   The following items are stored in Battery Backed Ram: Message of the Day and Attenuator startup states (default).   In Firmware revisions 3.2.0 and later, all of this information can be removed by using the WIPE command.   It removes all of this information and set the unit back to the factory default state.

_____   ___Sept. 5, 2014___
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Vice President